RoHS

COMPLIANT

**Vishay High Power Products** 

# Schottky Rectifier, 1.0 A



- Small foot print, surface mountable
- Very low forward voltage drop
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level

#### DESCRIPTION

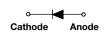
The VS-MBRS130LTRPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

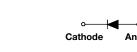
MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	1.0	А		
V <sub>RRM</sub>		30	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	230	А		
V <sub>F</sub>	1.0 Apk, T <sub>J</sub> = 125 °C	0.30	V		
TJ	Range	- 55 to 125	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-MBRS130LTRPbF	UNITS		
Maximum DC reverse voltage	V <sub>R</sub>	30	V		
Maximum working peak reverse voltage	V <sub>RWM</sub>		v		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I <sub>F(AV)</sub>	50 % duty cycle at $T_L$ = 106 °C, rectangular waveform		1.0	
Maximum peak one cycle non-repetitive surge current	I <sub>FSM</sub>	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V <sub>RRM</sub> applied	230	A
		10 ms sine or 6 ms rect. pulse		40	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1 A, L = 6 mH		3.0	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		1.0	А







PRODUCT SUMMARY				
I <sub>F(AV)</sub>	1.0 A			
V <sub>R</sub>	30 V			
I <sub>RM</sub>	20 mA at 125 °C			



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V <sub>FM</sub> <sup>(1)</sup>	1 A	T <sub>J</sub> = 25 °C	0.420	V
		2 A		0.470	
		1 A	T <sub>J</sub> = 125 °C	0.300	
		2 A		0.370	
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	1	mA
		T <sub>J</sub> = 100 °C		10	
		T <sub>J</sub> = 125 °C		20	
Maximum junction capacitance	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		200	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

<sup>(1)</sup> Pulse width < 300  $\mu$ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	T <sub>J</sub> <sup>(1)</sup>		- 55 to 125	°C
Maximum storage temperature range	T <sub>Stg</sub>		- 55 to 150	U
Maximum thermal resistance, junction to lead	R <sub>thJL</sub> <sup>(2)</sup>	DC operation See fig. 4	25	°C/W
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	80	C/W
Approvingete weight			0.10	g
Approximate weight			0.003	oz.
Marking device		Case style SMB (similar to DO-214AA)	V13L	

#### Notes

(1)  $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink

<sup>(2)</sup> Mounted 1" square PCB



DC

1.2

1.6

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Square wave (D = 0.50) 80 % rated  $V_R$  applied See note (1)

0.8

I<sub>F(AV)</sub> - Average Forward Current (A)

Fig. 4 - Maximum Average Forward Current vs.

0.4

130

120

110

100

90

0

D = 0.20

 $\begin{array}{l} \mathsf{D} = 0.25 \\ \mathsf{D} = 0.33 \\ \mathsf{D} = 0.50 \\ \mathsf{D} = 0.75 \end{array}$ 

Allowable Case Temperature (°C)

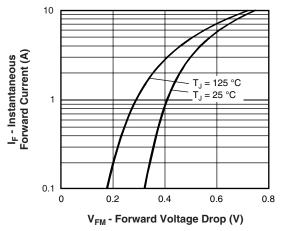
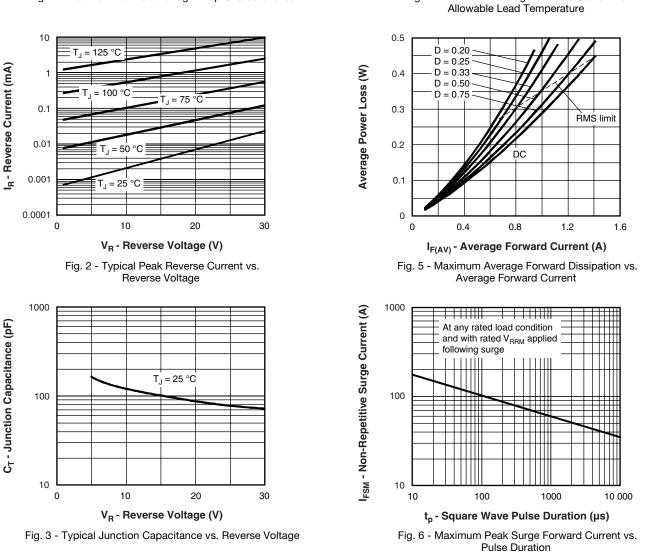


Fig. 1 - Maximum Forward Voltage Drop Characteristics



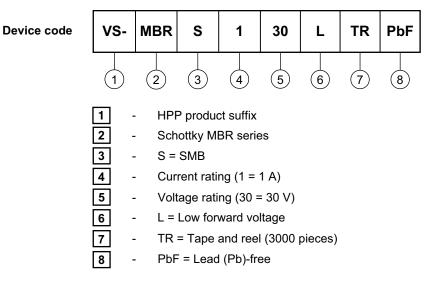
#### Note

- <sup>(1)</sup> Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$ ;
- Pd = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at ( $I_{F(AV)}/D$ ) (see fig. 6);  $Pd_{REV}$  = Inverse power loss =  $V_{R1} \times I_R$  (1 D);  $I_R$  at  $V_{R1}$  = 80 % rated  $V_R$

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#### ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95017			
Part marking information	www.vishay.com/doc?95029			
Packaging information	www.vishay.com/doc?95034			



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